

Static Program Analysis

Reaching Definitions

Winter Term 2014/15

Advanced Lecture (9 CP)

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Scale

```
(1) while(TRUE) {
(2)   if ((p_ab[CTRL2] & 0x10)==0) {
(3)     u = ((p_ab[PB] & 0x0f) << 8) + p_ab[PA];
(4)     u_kg = u * kal_kg;}
(5)   if ((p_cd[CTRL2] & 0x01) != 0) {
(6)     for (idx=0;idx<7;idx++) {
(7)       e_puf[idx] = p_cd[PA];
(8)       if ((p_cd[CTRL2] & 0x10) != 0) {
(9)         switch(e_puf[idx]) {
(10)          case '+': kal_kg *= 1.1; break;
(11)          case '-': kal_kg *= 0.9; break; } }
(12)       e_puf[idx] = '\0'; }
(13)   printf("Artikel: %07.7s\n",e_puf);
(14)   printf(" %6.2f kg ",u_kg);
(15)}
```



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```



Calibration factor can be changed!

Dataflow Analysis

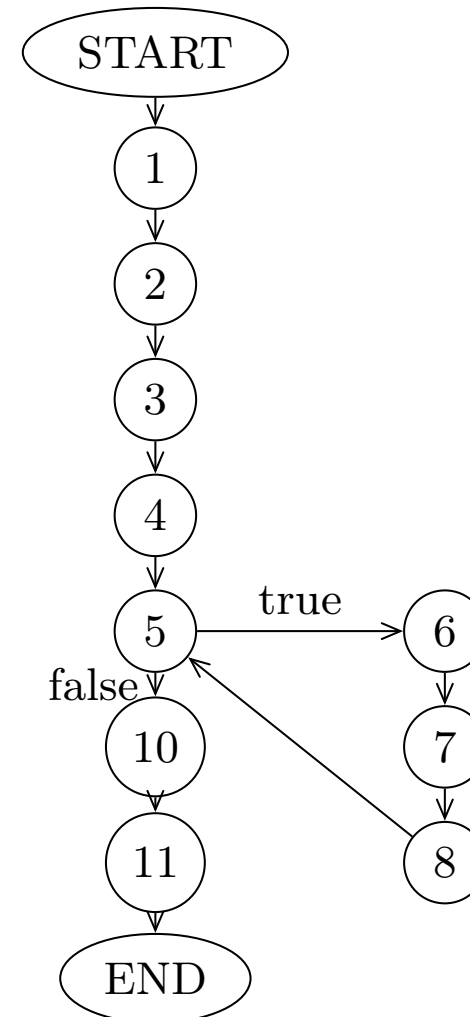
- Can a value computed at a certain statement flow to another given statement?
- usually represents program as a directed graph
- nodes are statements/predicates
- edges describe the control flow
- allows analysis of programs with
 - structured and
 - unstructured control flow
(break, continue, try ... catch ... finally, goto)

Intraprocedural Control Flow Graph

- Control Flow Graph (CFG) $G = (N, E, n_s, n_e, \nu)$
- Set of nodes N (statements and predicates)
- Distinguished nodes n_s and n_e
- Set of control flow edges $E, (n, m) \in E$
 $n \rightarrow_{cf} m$ iff m may execute directly after n
- Total attribute function $\nu : E \rightarrow \{\text{true}, \text{false}, \varepsilon\} \cup \mathbb{Z}$
condition under which control flows along an edge
- Functions *succ* and *pred* that map the successors and predecessors to each node.
- Reachability is undecidable, so conservatively assumed

Intraprocedural Control Flow Graph

```
(1) read(n);  
(2) i = 1;  
(3) sum = 0;  
(4) prod = 1;  
(5) while (i <= n) {  
(6)   sum = sum + i;  
(7)   prod = prod * i;  
(8)   i++;  
(9) }  
(10) write(sum);  
(11) write(prod);
```



Monotone Dataflow Analysis Framework

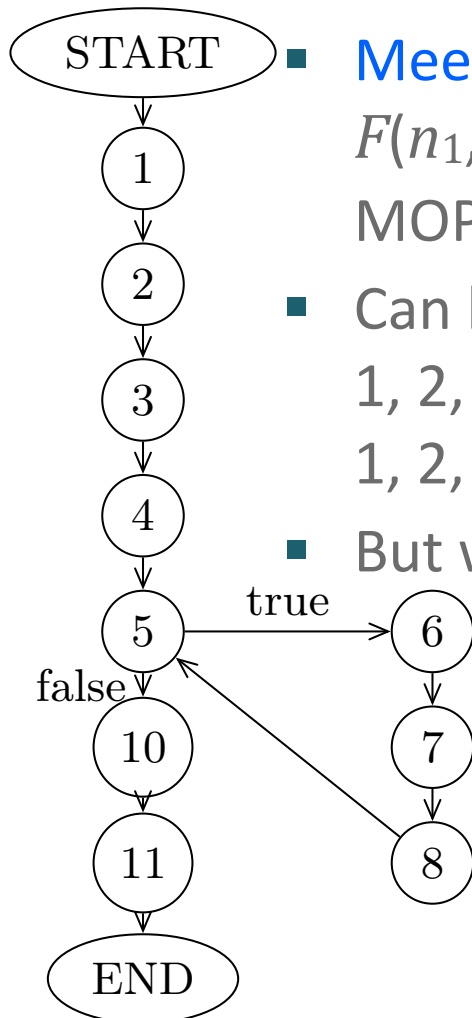
- most important class of dataflow analysis
- compute the most precise solution under certain conditions
- only takes a finite number of steps
 - even if loops have infinitely many paths
- Basic data structure: **complete lattice** $\mathcal{L} = (L, \sqsubseteq, \sqcup, \sqcap, \perp, \top)$

Monotone Function Space

- Set of functions \mathcal{F} on a meet semi-lattice $\mathcal{L} = (L, \sqsubseteq, \sqcap, \perp)$
- $\exists id_L \in \mathcal{F}: \forall x \in L: id_L(x) = x$ (identity function)
- $\forall F \in \mathcal{F}: \forall x, y \in L: x \sqsubseteq y \Rightarrow F(x) \sqsubseteq F(y)$ (monotonicity)
- $\forall F, G \in \mathcal{F}: F \circ G \in \mathcal{F}$ (closed under composition)
- $\forall F, G \in \mathcal{F}: F \sqcap G \in \mathcal{F}$ (pointwise infimum)

- Monotone Dataflow Analysis Framework
- Consists of a meet semi-lattice \mathcal{L}
- And a monotone function space \mathcal{F}
- **distributive**, if all functions are distributive over \sqcap :
$$\forall F \in \mathcal{F}: \forall x, y \in L: F(x \sqcap y) = F(x) \sqcap F(y)$$

Computing Data Flow Analyses



- **Meet-Over-All-Paths** (MOP) solution desired

$$F(n_1, \dots, n_k): L \rightarrow L : F(n_1, \dots, n_k)(x) = (Fn_k \circ F(n_1, \dots, n_{k-1}))(x)$$

MOP is $\prod_{\text{Path } P_i} F_{P_i}$

- Can be computed for 1, 2, 3, 4, 5, 10, 11;
1, 2, 3, 4, 5, **6, 7, 8**, 10, 11;
1, 2, 3, 4, 5, **6, 7, 8, 6, 7, 8**, 10, 11; etc.
- But when should we stop calculating?

Analysis time not linear with program size!

Fixed points

- **Fixed point** of an operator f on a semi-lattice L is an element $x \in L$ such that $f(x) = x$.
- **Maximal fixed point (MFP)** x if $\forall y \in L: f(y) = y \Rightarrow y \sqsubseteq x$.

Computing the MFP

```
foreach  $n \in CFG$  do
   $A[n] = \perp$ 
od
do
   $change = false$ 
  foreach  $n \in CFG$  do
     $temp = \bigsqcap_{q \in pred(n)} F_q(A[q])$ 
    if  $temp \neq A[n]$ 
       $change = true$ 
       $A[n] = temp$ 
    fi
  od
until ! $change$ 
```

- $fix(s) := A[s]$ after loop (Kildall '77)
- **Coincidence Theorem** (Kam, Ullman '77)

$$fix(s) = \bigsqcap_{\text{Path } P_i \text{ ending in } s} F_{P_i}(\perp)$$

if \mathcal{F} is distributive

- If \mathcal{F} is not distributive, the equality becomes \sqsubseteq , i.e., the fixed point is only a conservative approximation

Reaching definitions

- Reaching definitions is classical compiler problem
- Also prerequisite for computing dependence graphs
- Does a definition of a variable reach the use at a statement?
- Without being redefined underway?
- $Def(n)$ set of variables defined at n
- $Use(n)$ set of variables used at n
- Definition d , variable $v := var(d)$ where $v \in Def(n)$
- d reaches node n' if $\exists Path P = (n=n_0, \dots, n_k=n')$ in G , $k > 0$
 $\forall i \in 1, \dots, k - 1: v \notin Def(n_i)$

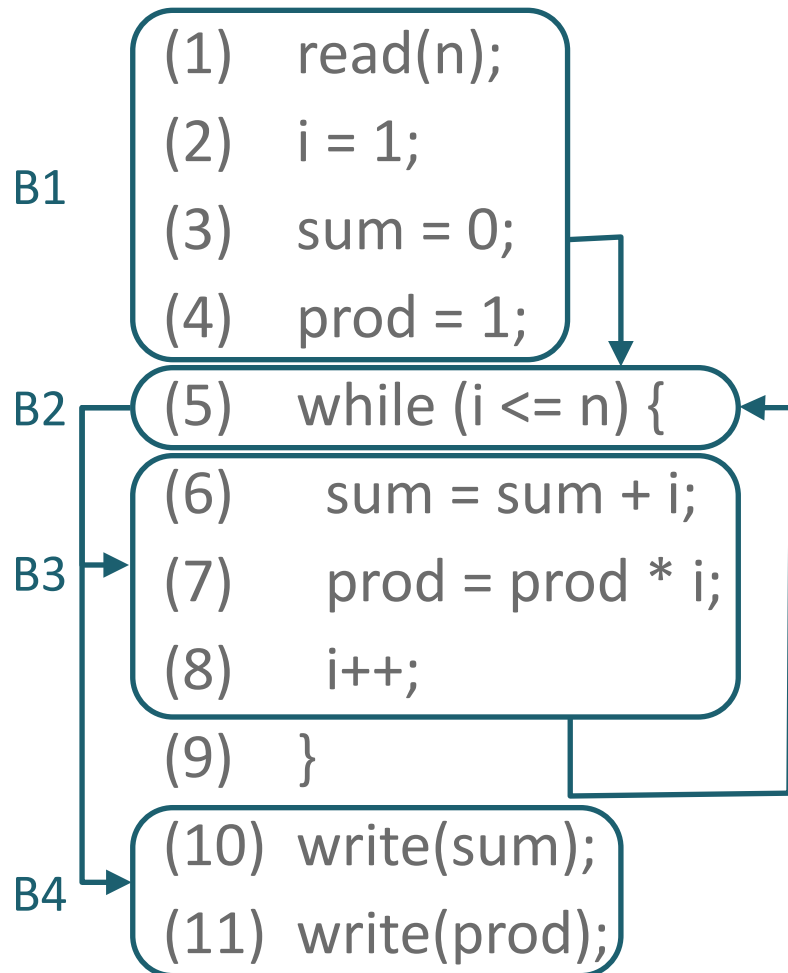
Reaching definitions as a DMDF

- $\mathcal{L} = (\mathcal{P}(D), \supseteq, \cup, \emptyset)$ (powerset of all definitions)
- Transfer functions derived from abstract semantics of variable assignment
- $F_n(X) = X \setminus \text{kill}(n) \cup \text{gen}(n)$
- Statement n defines variables in $\text{Def}(n)$, so all definitions in D defining the same variable can no longer be visible (killed)
$$\text{kill}(n) := \bigcup_{v \in \text{Def}(n)} D_v, \text{ where } D_v := \{d \in D \mid v = \text{var}(d)\}$$
- All elements of $\text{Def}(n)$ are generated by n , i.e. visible after its execution
$$\text{gen}(n) := \text{Def}(n)$$
- This MDFF is distributive (MOP and MFP coincide)

Basic Blocks

- Exactly one entry point
no code within is the destination of a jump instruction
- Exactly one exit point
only the last instruction can be a predicate
- Whenever the first instruction in a basic block is executed, the rest of the instructions are necessarily executed exactly once, in order
- Simplifies analysis when performed on basic blocks instead of statements

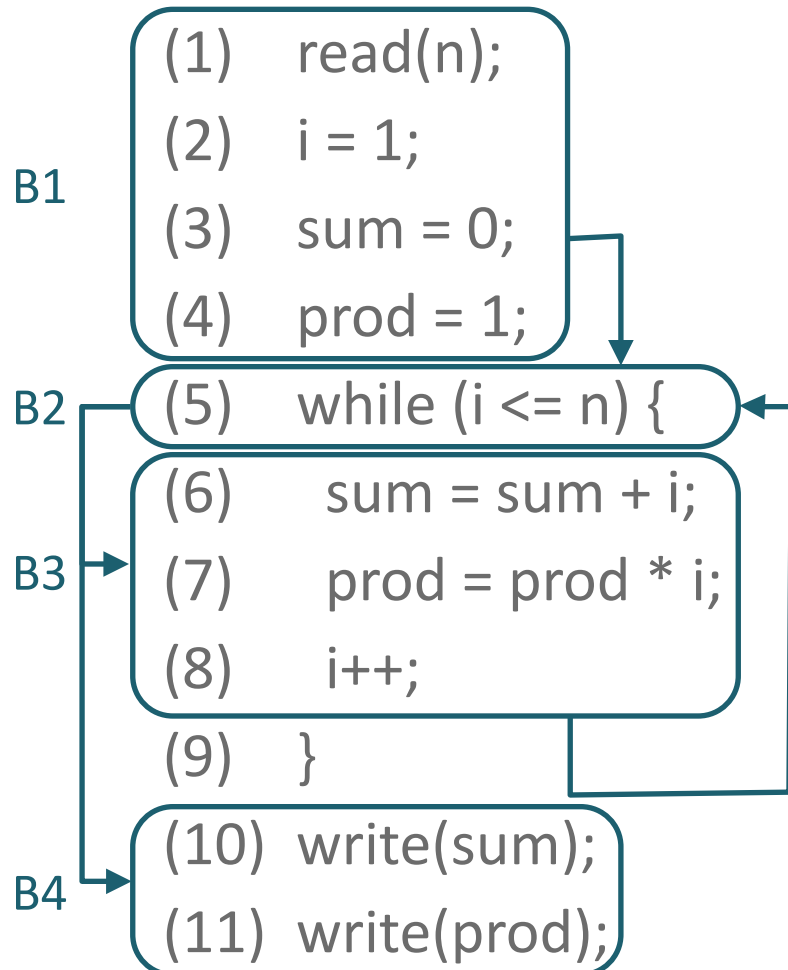
Example



- $\text{gen}(B1) = \{d1, d2, d3, d4\}$
- $\text{kill}(B1) = \{d6, d7, d8\}$
- $\text{use}(B1) = \emptyset$ //maybe $\{n\}$
- $\text{gen}(B2) = \emptyset = \text{kill}(B3);$
- $\text{use}(B2) = \{i, n\}$
- $\text{gen}(B3) = \{d6, d7, d8\}$
- $\text{kill}(B3) = \{d2, d3, d4\}$
- $\text{use}(B3) = \{i, \text{sum}, \text{prod}\}$
- $\text{gen}(B4) = \emptyset = \text{kill}(B4)$
- $\text{use}(B4) = \{\text{sum}, \text{prod}\}$

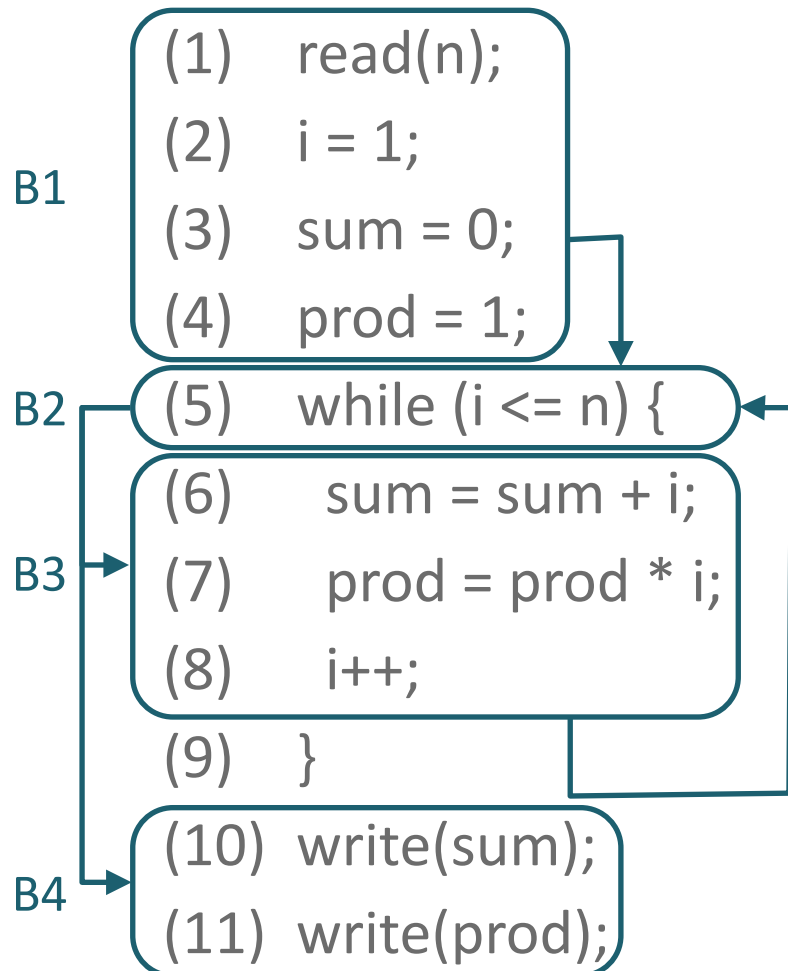
Example

in is called A
in the
algorithm



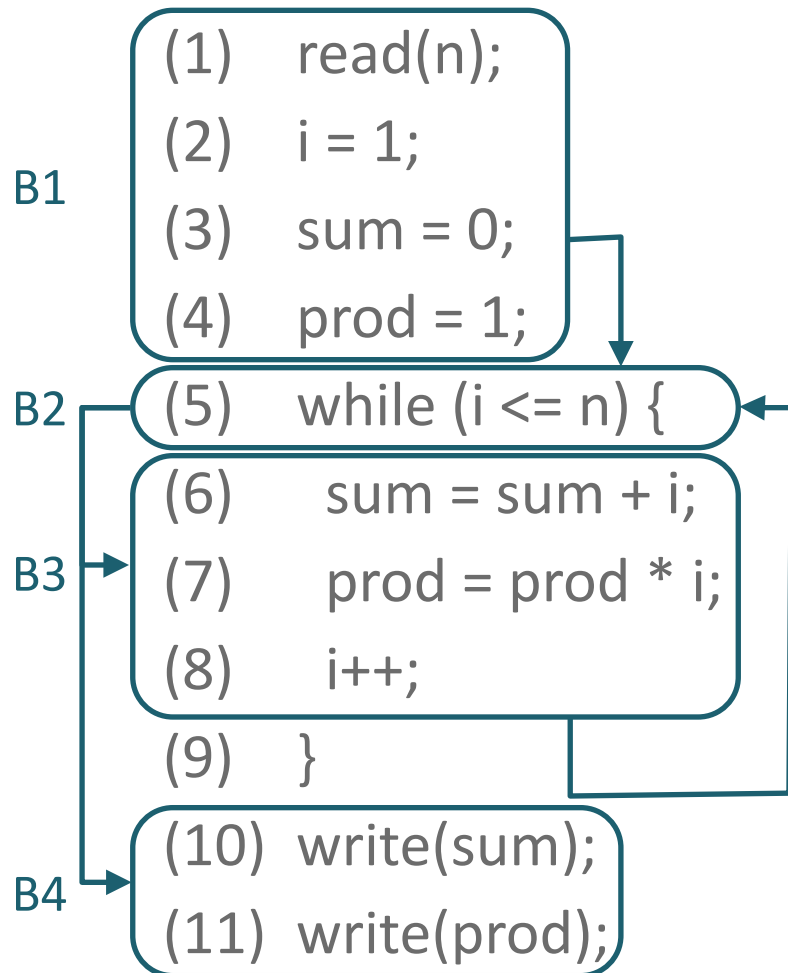
- Initialization: $in = [\emptyset, \emptyset, \emptyset, \emptyset]$
- $in(B1) = \emptyset$, $change = F$
- $in(B2) = out(B1) \cup out(B3) = F_{B1}(in(B1)) \cup F_{B3}(in(B3)) = (\emptyset \setminus \{d6, d7, d8\} \cup \{d1, d2, d3, d4\}) \cup (\emptyset \setminus \{d2, d3, d4\} \cup \{d6, d7, d8\}) = \{d1, d2, d3, d4, d6, d7, d8\}$
 $change = T$
- $in(B3) = out(B2) = F_{B2}(in(B2)) = \{d1, d2, d3, d4, d6, d7, d8\} \setminus \emptyset \cup \emptyset = \{d1, d2, d3, d4, d6, d7, d8\}$,
 $change = T$

Example



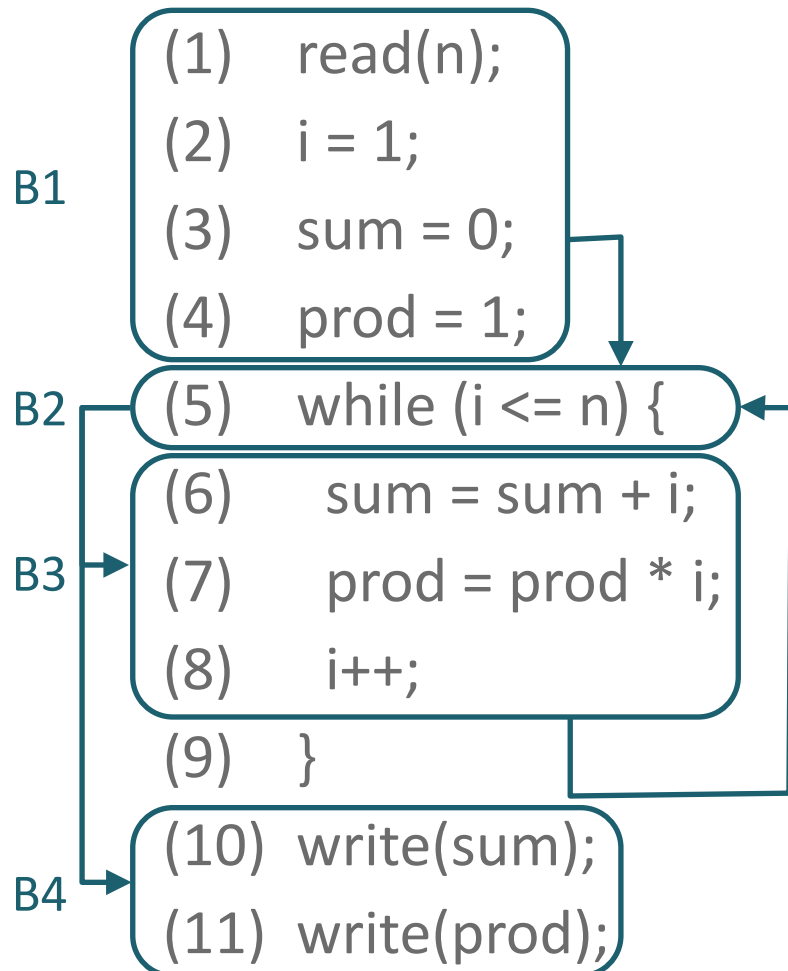
- $\text{in}(B1) = \emptyset$
- $\text{in}(B2) = \{d1, d2, d3, d4, d6, d7, d8\}$
- $\text{in}(B3) = \{d1, d2, d3, d4, d6, d7, d8\}$
- $\text{in}(B4) = \text{out}(B2) = F_{B2}(\text{in}(B2)) = \{d1, d2, d3, d4, d6, d7, d8\} \setminus \emptyset \cup \emptyset = \{d1, d2, d3, d4, d6, d7, d8\}$
change = T
- ...

Example



- $\text{in}(B1) = \emptyset$, $\text{change} = F$
- $\text{in}(B2) = \text{out}(B1) \cup \text{out}(B2) = \{d1, d2, d3, d4, d6, d7, d8\}$, $\text{change} = F$
- $\text{in}(B3) = \text{out}(B2) = \{d1, d2, d3, d4, d6, d7, d8\}$, $\text{change} = F$
- $\text{in}(B4) = \text{out}(B2) = \{d1, d2, d3, d4, d6, d7, d8\}$, $\text{change} = F$
- Algorithm terminates here
- need to use transfer functions in the block to determine reaching definitions for statements

Example



- $\text{in}(B1) = \emptyset$
- $\text{in}(B2) = \{d1, d2, d3, d4, d6, d7, d8\}$
- $\text{in}(B3) = \{d1, d2, d3, d4, d6, d7, d8\}$
- $\text{in}(B4) = \{d1, d2, d3, d4, d6, d7, d8\}$
- at beginning of B2 all definitions are visible, in particular both definitions of i , sum , and $prod$
- But:
 $\text{in}(7) = F_6(\text{in}(B3)) = \{d1, d2, d4, d6, d7, d8\}$